One Day Workshop
On
FPGA Programmable Soc for Electrical Control Systems
5th December, 2014

REGISTRATION FORM

1. Name : 
2. Age : 
3. Gender : 
4. Qualification : 
5. Designation : 
6. Name of the Institution : 
7. Department : 
8. Is the institution affiliated to Anna University : Yes / No
9. Address for communication : 
10. Mobile Number : 
11. E-mail address : 
12. Experience (in years) : Teaching : Others : 
12. Demand Draft No : 

The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the training programme.

Place: 
Date: 
Signature of the applicant

Signature & Seal 
of the Head of the Institution

REGISTRATION FEES

<table>
<thead>
<tr>
<th>Category</th>
<th>INR</th>
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</thead>
<tbody>
<tr>
<td>Research Scholars, PG Students &amp; Faculty from Academic Institutions</td>
<td>500</td>
</tr>
<tr>
<td>Participants from Industry / R&amp;D Organizations</td>
<td>800</td>
</tr>
</tbody>
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DD Drawn in favor of “Sona College of Technology”, payable at Salem.

Registration fee includes Certificate, Working Lunch, Tea & Snacks. Completely filled in application (attached with this pamphlet) form should be sent to the Coordinator on or before the specified dates. Selected participants will be informed through E-mail and should confirm their acceptance of participation. No TA/DA will be provided.

Application form complete in all respects is to be sent to:

Dr. R. Arulmozhiyal
Coordinator
Professor, Department of Electrical and Electronics Engineering Sona College of Technology
(An Autonomous Institution)
Salem- 636005.
Phone: 0427- 4099720
Email: arulmozhiyal@gmail.com

For further enquiries and communication, contact:
Mr. P. Manikandan & Mr. M. Murali
Mobile: 9944270473, 7639916789
Website: www.sonatech.ac.in

One Day Workshop
On
FPGA Programmable Soc for Electrical Control Systems
5th December, 2014

ORGANIZED BY

CONVENOR
Dr. C. Easwarlal

CO-ORDINATOR
Dr. R. Arulmozhiyal

Department of Electrical and Electronics Engineering Sona College of Technology
(An Autonomous Institution)
Salem-626005
Phone: 0427 – 4099720

In Association with
THE OBJECTIVE

This workshop provides an introduction to the advanced tools to design and implement controller in FPGAs using Xilinx. The Main focus of workshop is to learn how to use system generator tools and model the system generator tool for electrical applications through hands-on exercises; thus, you will be able to develop your custom models which can be simulated and interfaced with FPGA. To implement controller designs from algorithm to Real-time hardware implementation. To introduce Zynq 7000 programmable SoC and design efficiency of Xilinx FPGA tools possible applications in the field of controller and drives. After the completion of this training program the participants will be able to:

- Understand why FPGAs are preferred over DSP’s to address high-performance DSP design implementations
- Describe the basics of system modeling using Simulink
- Model and simulate a system model using Simulink/System Generator
- Identify the solutions from Xilinx DSP design flow for addressing image, audio and signal processing applications

ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was established in the year 1997 with an aim to produce quality engineers to meet the recent trends in the field of Electrical and Electronics Engineering and headed by experienced and inspiring personality Dr.C.Easwarlal. SONASPEED and SONAPERT are the research centers of this department, working in developing BLDC motors for ISRO and modules for locating faults in the underwater cables respectively. SONAPEDAC is another research wing of this department, working in Controller and Drives.

ABOUT COREEL TECHNOLOGIES

CoreEL Technologies (I) Pvt Ltd, CoreEL is a customer Application Specific Products & Solutions company offering Intellectual Property (IP) Hardware, Software & Engineering Services to customers, enabling them to Design Manufacture and Market world class electronic products. The portfolio of offerings include IP cores, Sytem Design, Architecture, Validation, Sustenance, Prototype Manufacturing, Next-Gen products, Semiconductor solutions & Distribution of EDA Tools & COTS products. CoreEL was founded in 1999 and is an ISO 9001:2008 certified headquartered at Bangalore, India.

ELIGIBILITY

Faculties from AICTE approved Engineering Colleges with relevant background. Candidates from industries and R & D organizations will also be considered. PG students & Research Scholars in related discipline are also eligible.

SCHEDULED DATE

Last date for receipt of application: 01.12.2014
Intimation of Selection by email: 02.12.2014
Confirmation by Participants: 03.12.2014

RESOURCE PERSONS

Mr Prakash G – Product Specialist
Mr P.Gowrishankar Coreel Technologies Bangalore. Tel: 09940716782.

COURSE OUTLINE

Session I: Introduction to Xilinx System Generator for FPGA Concepts System Generator of system modeling using Simulink.

Session II: Introduction to Zynq 7000 all programmable SOC and Embedded system design Concepts of Hardware co-simulation using System Generator FPGA. Model and simulate a FPGA block using Simulink/ Xilinx System generator.

Lab 1: Getting started with Simulink Embedded system lab demo using Zed board with Zync 7000 SOC Usage of Chipscope for embedded system debugging.

Lab 2: System Generator demo using zed board Zync 7000 SOC.