Sona College of Technology, Salem-5

Department of Electronics and Communication Engineering

Date: 03-05-2018

Action taken report for stakeholders' recommendations

S.No	Stakeholders	Recommendations	Action To be Taken
1.	Industry Experts	For the course Microwave Engineering Mr. M. Kumar asked to the title for unit IV is renamed as planar microwave transmission lines and the topic coplanar microwave transmission lines is added.	Unit IV is to be renamed as planar microwave transmission lines
2.	Alumni	Verilog HDL code for Registers and counters can be added	Verilog HDL code for Registers and counters to be added
3.	Students	Ms. M. Priyanka asked VHDL may be given more hours in the course VLSI Design	For the course VLSI Design more hours can be planned for VHDL
4.	Employers	The course Embedded Systems Laboratory experiments are in higher level.	The course Embedded Systems Laboratory experiments considered as case study
5.	Parents	Requested to provide latest technologies in the field of ECE	Recent technologies such as Internet of things etc. to be added in the course Embedded and Real time systems
6.	Academic Experts	Dr.K.R.Kavitha requested that the Mini project is moved to VII semester and the courses Embedded Systems and Embedded Systems Laboratory shifted to VI semester	Mini project is to be moved to VII semester

Signature of Academic Coordinators

Prof.J.Harirajkumar
Dr.S.Vijayalakshmi

BOS Chairman

Dr.R.S.Sabeenian

Dr.R.S.SABEENIAN, M.E.,MBA.,Ph.D.,FIETE,
Professor and Head of Department
Electronics and Communication Engineering
SONA COLLEGE OF TECHNOLOGY,
Salem - 636 005. Tamilnadu, India.

Sona College of Technology, Salem-5

Department of Electronics and Communication Engineering

Date: 21.11.18

Action taken report for stakeholders' recommendations.

S.No	Stakeholders	Recommendations	Action To be Taken
1.	Industry Experts	Mr. V. Vinoth discussed about the upcoming industry oriented courses for the regulations 2015 and 2015R.	VLSI design and VLSI design lab may be added in the even semester
2.	Alumni	Study of advanced processors can be included in the course Microprocessor and Microcontroller	Study of advanced Microprocessor to be included in Unit 5 in the course Microprocessor and Microcontroller
3.	Students	Students suggested to include case study in the core courses	Case studies to be included in the core courses
4.	Employers	Mr.V.Vinoth suggested to include port measurements in the course Microwave Engineering	Port measurements to be included in course Microwave Engineering
5.	Parents	Requested to provide other department courses as elective	Other department courses provided as an Open elective course
6.	Academic Experts	Prof.T.Shanthi informed to change elective course Digital Image Processing and Digital Image Processing Lab to core course	The course Digital Image Processing and Digital Image Processing Lab to be changed as core course

Signature of Academic Coordinators

Prof.J.Harirajkumar
Dr.S.Vijayalakshmi

BOS Chairman

Dr.R.S.Sabeenian